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| US 20010032029 A1 | Method and apparatus for analyzing a design<br>System and method for infrastructure design  | 20011018 | 700/99   |
| US 6697633 B1     | Method permitting increased frequency re-use in a communication network, by recovery of transmitted information from multiple cochannel signals | 20040224 | 455/509  |
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| US 6535666 B1     | Method and apparatus for separating signals transmitted over a waveguide  | 20030318 | 385/31   |
| US 6310704 B1     | Communication apparatus for transmitting and receiving signals over a fiber-optic waveguide using different frequency bands of light            | 20011030 | 398/9    |
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| US 6208295 B1     | Method for processing radio signals that are subject to unwanted change during propagation  | 20010327 | 342/378  |
| US 6018317 A      | Cochannel signal processing system  | 20000125 | 342/378  |

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| US 6697633 B1     | Method permitting increased frequency re-use in a communication network, by recovery of transmitted information from multiple cochannel signals | 20040224 | 455/509  |
| US 6658234 B1     | Method for extending the effective dynamic range of a radio receiver system   | 20031202 |          |
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| US 6310704 B1     | Communication apparatus for transmitting and receiving signals over a fiber-optic waveguide using different frequency bands of light            | 20011030 | 398/9    |
| US 6215983 B1     | Method and apparatus for complex phase equalization for use in a communication system   | 20010410 | 455/63.1 |
| US 6208295 B1     | Method for processing radio signals that are subject to unwanted change during propagation  | 20010327 | 342/378  |
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## Results of search set L7:

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| US 6691077 B1     | Capture and conversion of mixed-signal test stimuli   | 20040210 | 703/4  |
| US 6675135 B1     | Six sigma design method   | 20040106 | 703/2  |
| US 6571202 B1     | Method for applying design for reliability into design for six sigma                                      | 20030527 | 703/2  |
| US 6477471 B1     | Product defect predictive engine  | 20021105 | 702/34 |
| US 6018623 A      | Method and system for determining statistically based worst-case on-chip interconnect delay and crosstalk | 20000125 | 716/6  |
| US 5181179 A      | Artificial parameter homotopy methods for the dc operating point problem                                  | 19930119 | 703/14 |